

6/18/14

<b>FORM PTO-1449</b>  <b>INFORMATION DISCLOSURE CITATION IN AN APPLICATION</b> Express Mail Number <u>EV364033441US</u> (Use several sheets if necessary)	Docket Number (Optional) 100-15120 (P04976-P01-D01)	Application No. New (Div. of 09/994,293)
	Applicant(s) Abdalla Aly Naem	
	Filing Date Herewith	Group Art Unit Unknown

U.S. PATENT DOCUMENTS												
EXAMINER INITIAL	DOCUMENT NUMBER							DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
EO	*5	3	2	3	0	3	2	06/21/94	Sato et al.	257	198	09/08/92
	*5	3	2	0	9	7	2	06/14/94	Wylie	437	31	01/07/93
	*4	5	6	4	9	9	7	01/21/86	Matsuo et al.	29	576W	04/16/82
	*6	3	5	2	9	0	7	03/05/02	Gris	438	438	05/26/00
	*6	1	7	1	9	3	6	01/09/01	Fitzgerald	438	503	01/04/99
	*5	8	9	7	3	5	9	04/27/99	Cho et al.	438	312	12/09/97
	*5	6	4	8	2	8	0	07/15/97	Kato	437	31	09/26/95
	*5	3	6	2	6	6	9	11/08/94	Boyd et al.	437	67	06/24/93
	*5	0	7	7	2	2	7	12/31/91	Kameyama et al.	437	31	08/08/90
	*6	4	2	6	2	6	5	07/30/02	Chu et al.	438	312	01/30/01
	*5	5	0	8	2	1	3	04/16/96	Van Der Wel et al.	437	31	10/20/94
EO	*5	1	8	7	5	5	4	02/16/93	Miwa	307	586	04/29/91

FOREIGN PATENT DOCUMENTS												
	DOCUMENT NUMBER					DATE	COUNTRY	CLASS	SUBCLAS S	Translation		
										Yes	No	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)												
EO	C.A. King et al., "Very Low Cost Graded SiGe Base Bipolar Transistors for a High-Performance Modular BiCMOS Process", IEDM, 1999, pps. 565-568.											
EO	Wim van der Wel et al., "Poly-Ridge Emitter Transistor (PRET): Simple Low-Power Option to a Bipolar Process", IEDM, 1993, pps. 453-456.											

EXAMINER	EO	DATE CONSIDERED	3-5-05
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP §609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.			

# INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

Express Mail Number EV36403344115

Docket Number (Optional)

100-15120 (P04976-P01-D01)

Application Number

New (Div. of 09/994,293)

Applicant(s)

Abdalla Aly Naem

Filing Date

Herewith

Group Art Unit

Unknown

## U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
60		*6,528,861 B1	03/04/03	Naem	257	565	02/04/02
		*6,337,251 B1	01/08/02	Hashimoto	438	309	03/29/00
50		*6,333,235 B1	12/25/01	Lee et al.	438	309	04/12/00

## FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
20		JP04082268	03/16/92	Japan	H01L	27/14		✓
		JP02260430	10/23/90	Japan	H01L	21/331		✓
		JP06318602	11/15/94	Japan	H01L	21/331		✓
50		EP0779,663 A2	06/18/97	Europe	H01L	29/732		✓

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

20		IBM Technical Disclosure Bulletin, "Epi-Based Bipolar Transistor With Oxide-Defined Collector Window", Vol. 34, No. 1, June 1991, pps. 422-424.

EXAMINER

E. 82

DATE CONSIDERED

3-5-05

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